ACKNOWLEDGEMENT RECEIPT

Electronic Version

Stylesheet Version v01

Title of Invention	STRUCTURE AND METHOD OF MAKING STRAINED CHANNEL CMOS TRANSISTORS HAVING LATTICE-MISMATCHED EPITAXIAL EXTENSION AND SOURCE AND DRAIN REGIONS

Submission Type :   Utility Patent Filing

Application Number:

     10/605134

EFS ID: 47512

Server Response:

Confirmation Code	Message
ISVR1	Submission was successfully submitted - Even if Informational or Warning Messages appear below, please do not resubmit this application
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First Named Applicant: Huajie Chen

Attorney Docket Number: FIS920030198US1

Timestamp: 2003-09-10 15:34:02 EDT

From: us

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